

FIG. 1

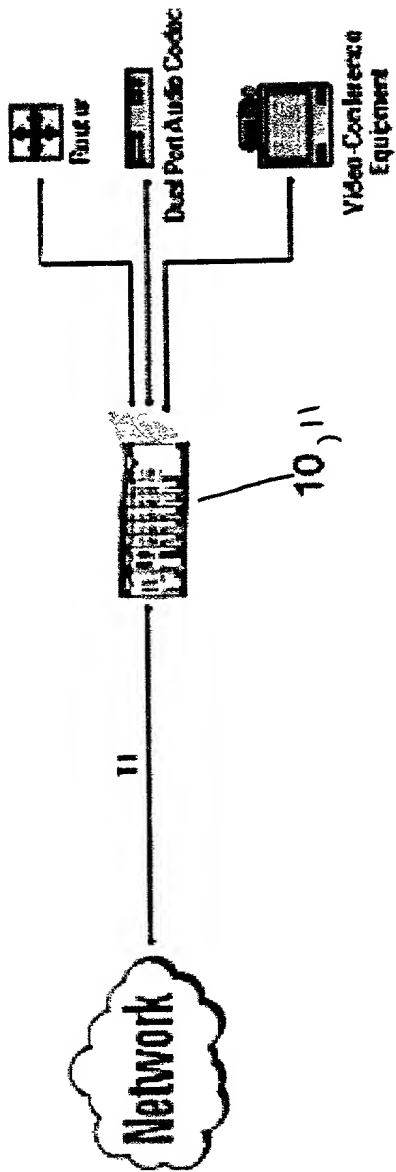


FIG. 2

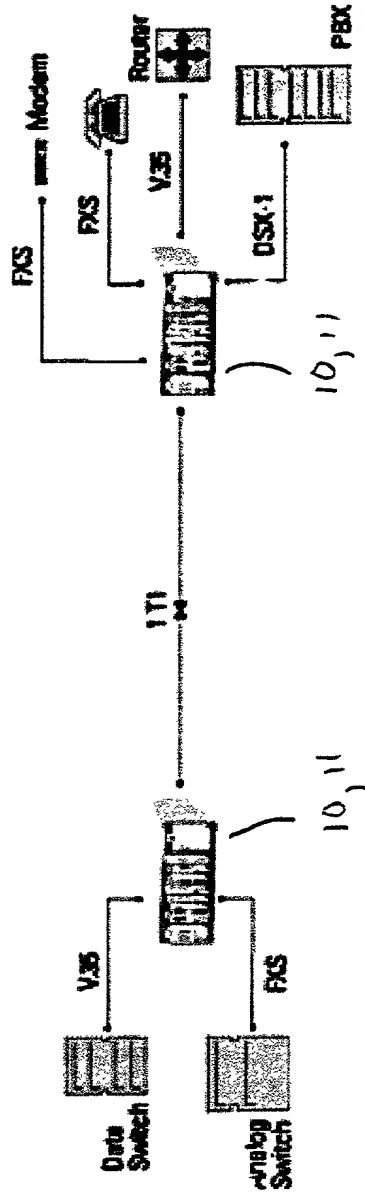


FIG. 3

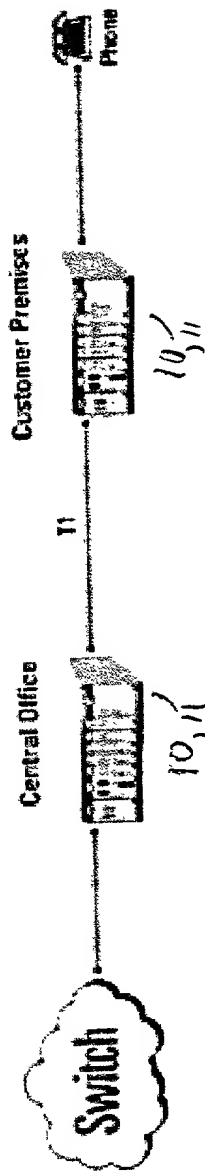


FIG. 4(a)

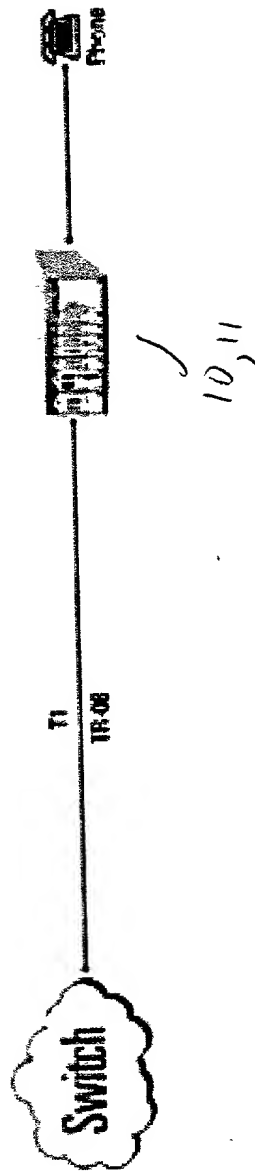


FIG. 4(b)

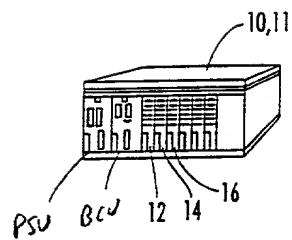


FIG. 5

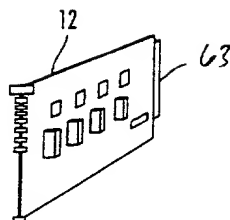


FIG. 6

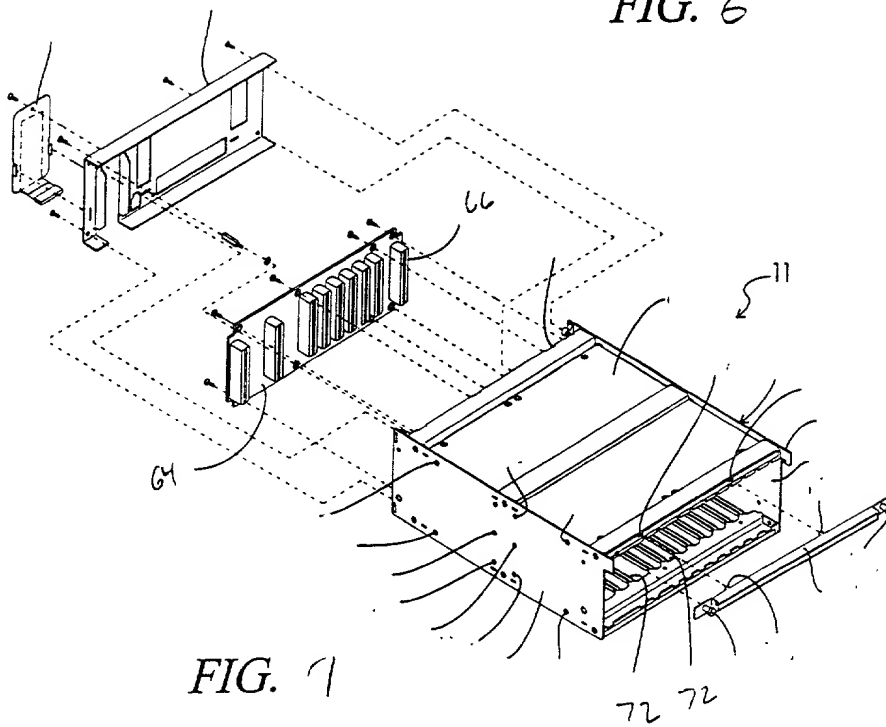


FIG. 7

09334939 094304

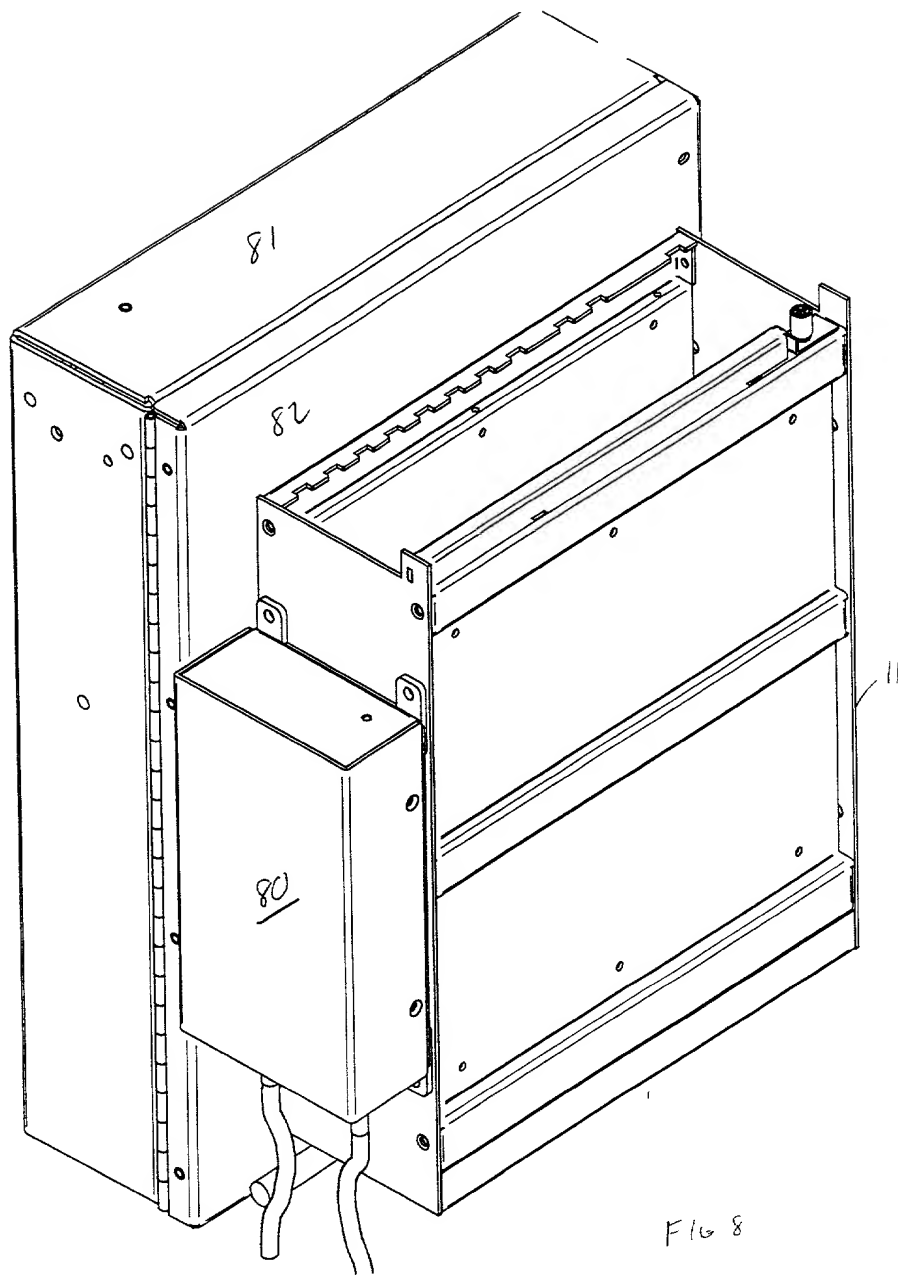


FIG 8

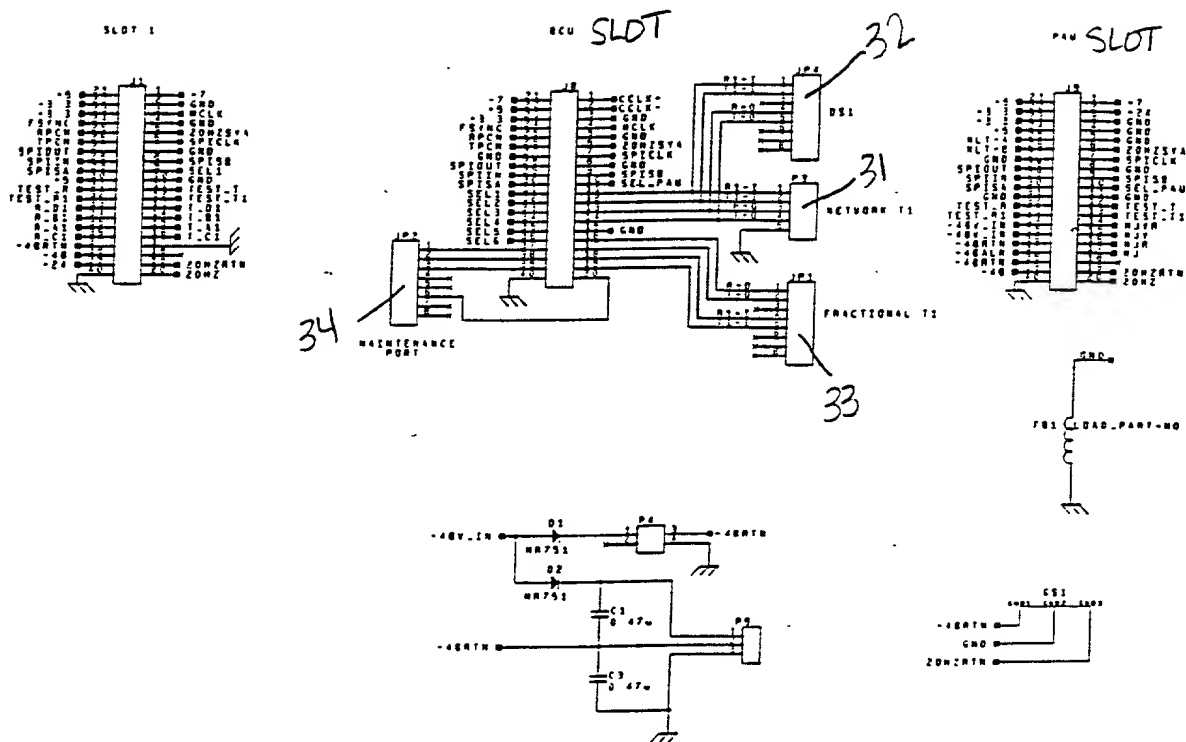
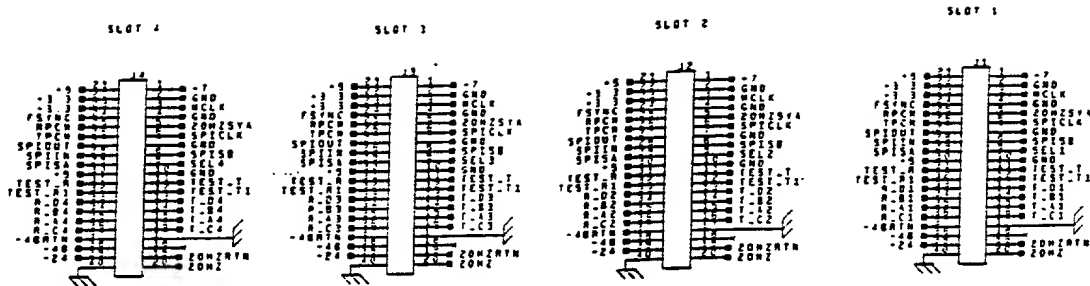


Figure 9

06044533-004304



34

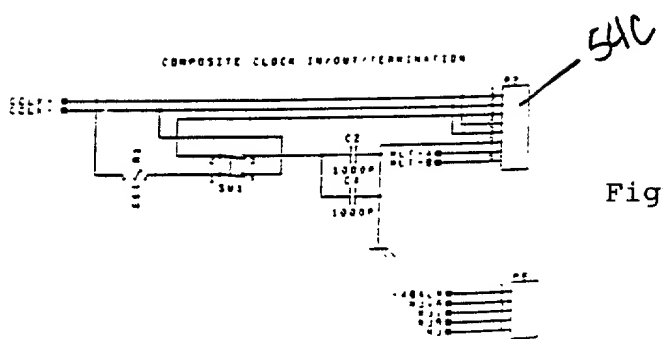
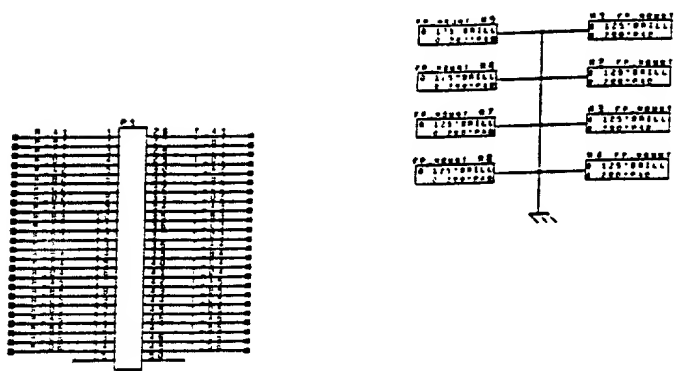


Figure 9 (Continued)

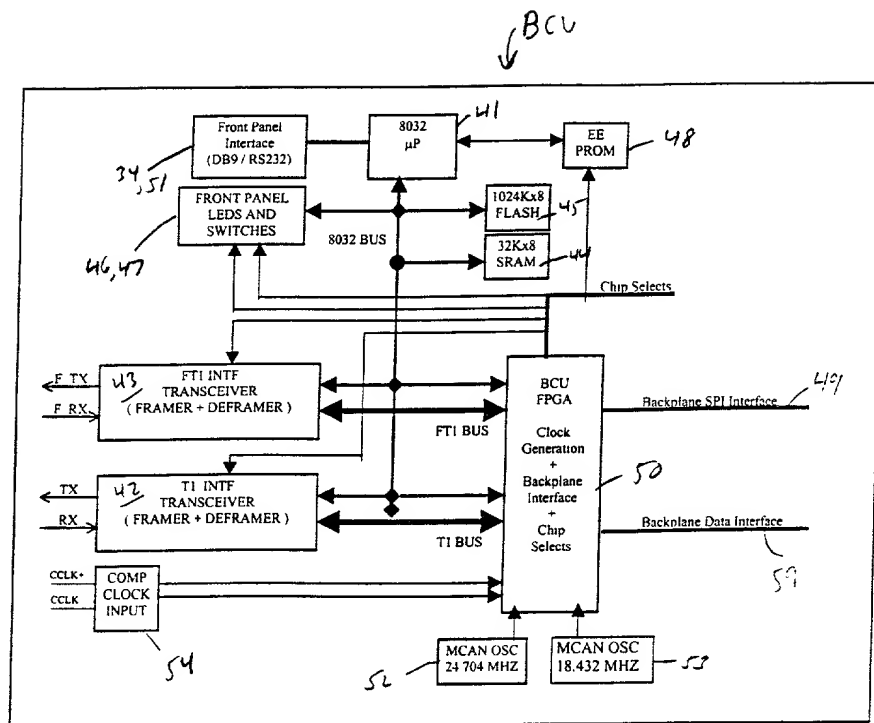
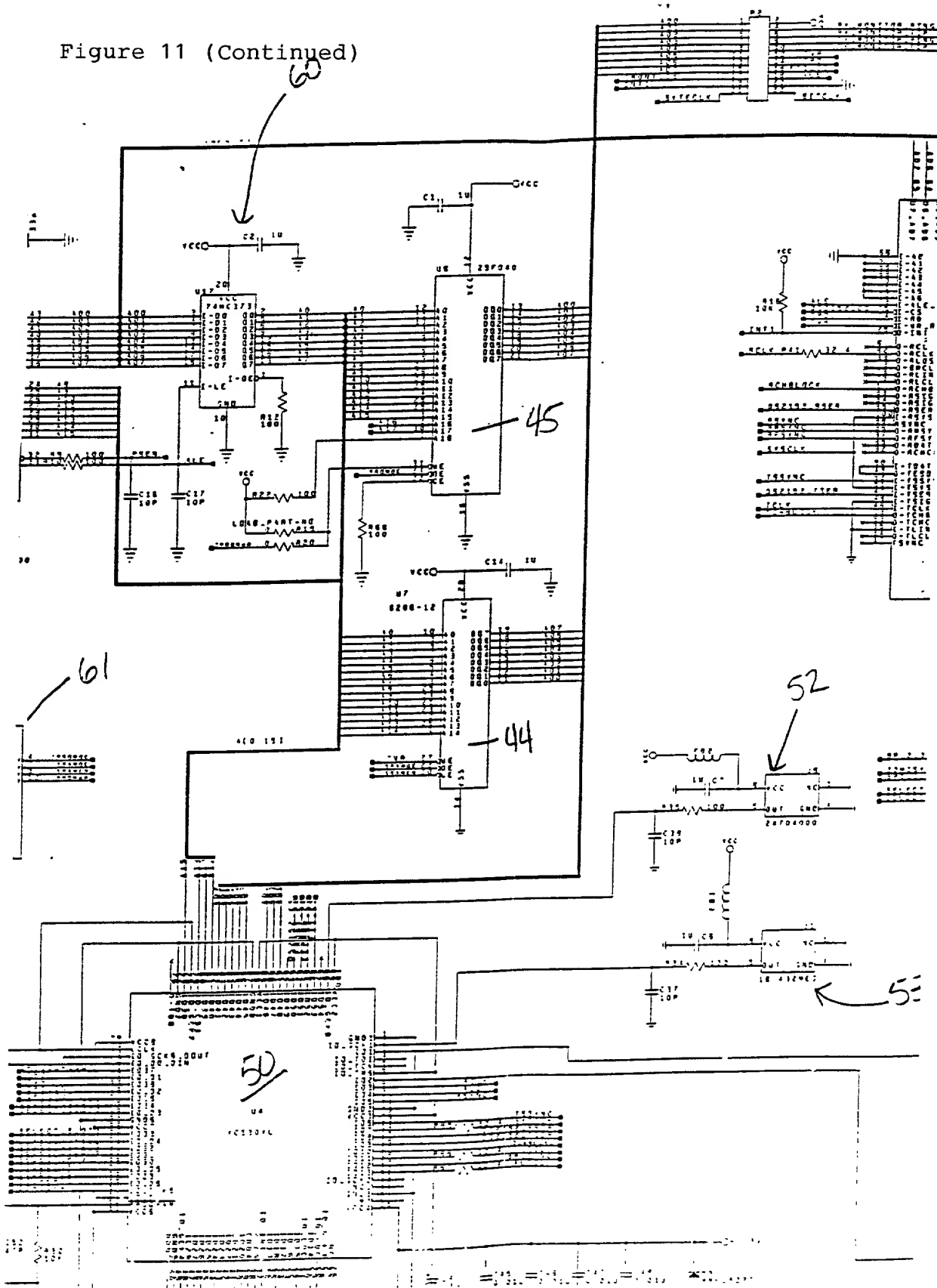


Fig. 10

The diagram is a hand-drawn schematic of a digital circuit. At the top center is a microcontroller labeled '51'. To its left is a 74151 decoder labeled '41'. Below the microcontroller is a 74153 decoder labeled '62'. To the right of the microcontroller is a 74154 decoder labeled '48'. The circuit includes various components such as resistors (e.g., 10K, 1K, 100, 1000), capacitors (e.g., 100pF, 10nF, 1uF), and logic gates. Power supply connections for VCC and GND are shown throughout the circuit. The diagram is annotated with handwritten numbers 34, 41, 48, 51, 61, and 62, which likely correspond to component identifiers or pin numbers.

Figure 11

Variable	Mean	Std. Dev.	Minimum	Maximum
Age	34.50	10.50	20	50
Gender	1.50	.50	1	2
Marital Status	1.50	.50	1	2
Education	15.50	2.50	10	20
Income	1.50	.50	1	2
Occupation	1.50	.50	1	2
Religion	1.50	.50	1	2
Health	1.50	.50	1	2
Stress	1.50	.50	1	2
Depression	1.50	.50	1	2
Loneliness	1.50	.50	1	2
Life Satisfaction	1.50	.50	1	2
Self-Esteem	1.50	.50	1	2
Resilience	1.50	.50	1	2
Optimism	1.50	.50	1	2
Gratitude	1.50	.50	1	2
Forgiveness	1.50	.50	1	2
Empathy	1.50	.50	1	2
Compassion	1.50	.50	1	2
Kindness	1.50	.50	1	2
Patience	1.50	.50	1	2
Humility	1.50	.50	1	2
Modesty	1.50	.50	1	2
Generosity	1.50	.50	1	2
Generous	1.50	.50	1	2
Altruistic	1.50	.50	1	2
Selfish	1.50	.50	1	2
Envy	1.50	.50	1	2
Jealousy	1.50	.50	1	2
Anger	1.50	.50	1	2
Aggression	1.50	.50	1	2
Violence	1.50	.50	1	2
Crime	1.50	.50	1	2
Substance Use	1.50	.50	1	2
Alcohol	1.50	.50	1	2
Drugs	1.50	.50	1	2
Gambling	1.50	.50	1	2
Smoking	1.50	.50	1	2
Exercise	1.50	.50	1	2
Healthy Diet	1.50	.50	1	2
Sleep	1.50	.50	1	2
Hygiene	1.50	.50	1	2
Personal Care	1.50	.50	1	2
Appearance	1.50	.50	1	2
Style	1.50	.50	1	2
Fashion	1.50	.50	1	2
Beauty	1.50	.50	1	2
Makeup	1.50	.50	1	2
Hair	1.50	.50	1	2
Nails	1.50	.50	1	2
Skincare	1.50	.50	1	2
Cosmetics	1.50	.50	1	2
Perfume	1.50	.50	1	2
Accessories	1.50	.50	1	2
Jewelry	1.50	.50	1	2
Shoes	1.50	.50	1	2
Clothing	1.50	.50	1	2
Home Decor	1.50	.50	1	2
Furniture	1.50	.50	1	2
Art	1.50	.50	1	2
Music	1.50	.50	1	2
Dance	1.50	.50	1	2
Sports	1.50	.50	1	2
Travel	1.50	.50	1	2
Vacation	1.50	.50	1	2
Adventure	1.50	.50	1	2
Relaxation	1.50	.50	1	2
Leisure	1.50	.50	1	2
Entertainment	1.50	.50	1	2
Media	1.50	.50	1	2
Technology	1.50	.50	1	2
Smartphone	1.50	.50	1	2
Tablet	1.50	.50	1	2
Laptop	1.50	.50	1	2
Smart TV	1.50	.50	1	2
Gaming Console	1.50	.50	1	2
Smartwatch	1.50	.50	1	2
Wearable Device	1.50	.50	1	2
Smart Home	1.50	.50	1	2
Robot Vacuum	1.50	.50	1	2
Smart Speaker	1.50	.50	1	2
Smart Lock	1.50	.50	1	2
Smart Thermostat	1.50	.50	1	2
Smart Lighting	1.50	.50	1	2
Smart Irrigation	1.50	.50	1	2
Smart Garage Door	1.50	.50	1	2



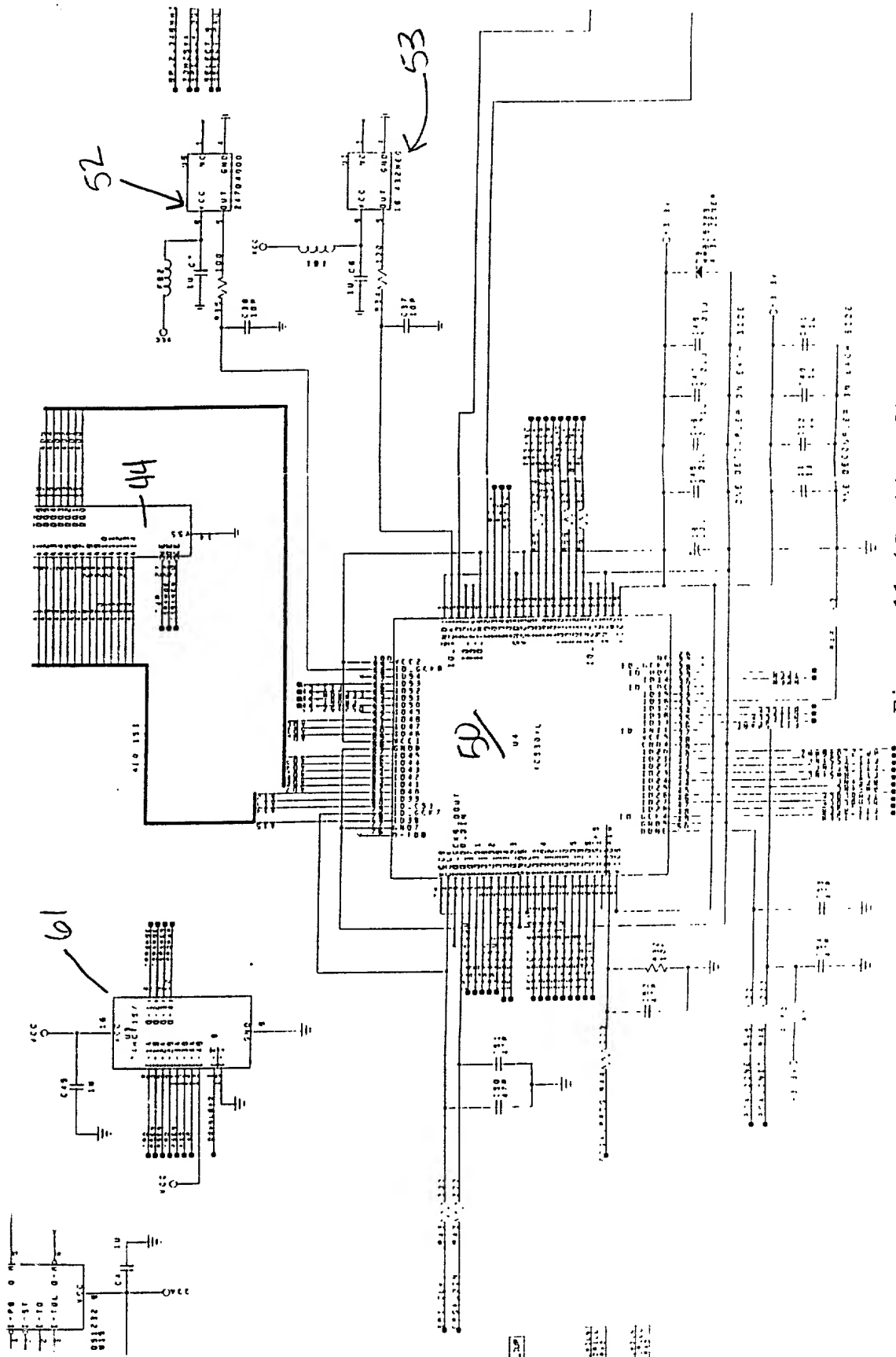


Figure 11 (Continued)

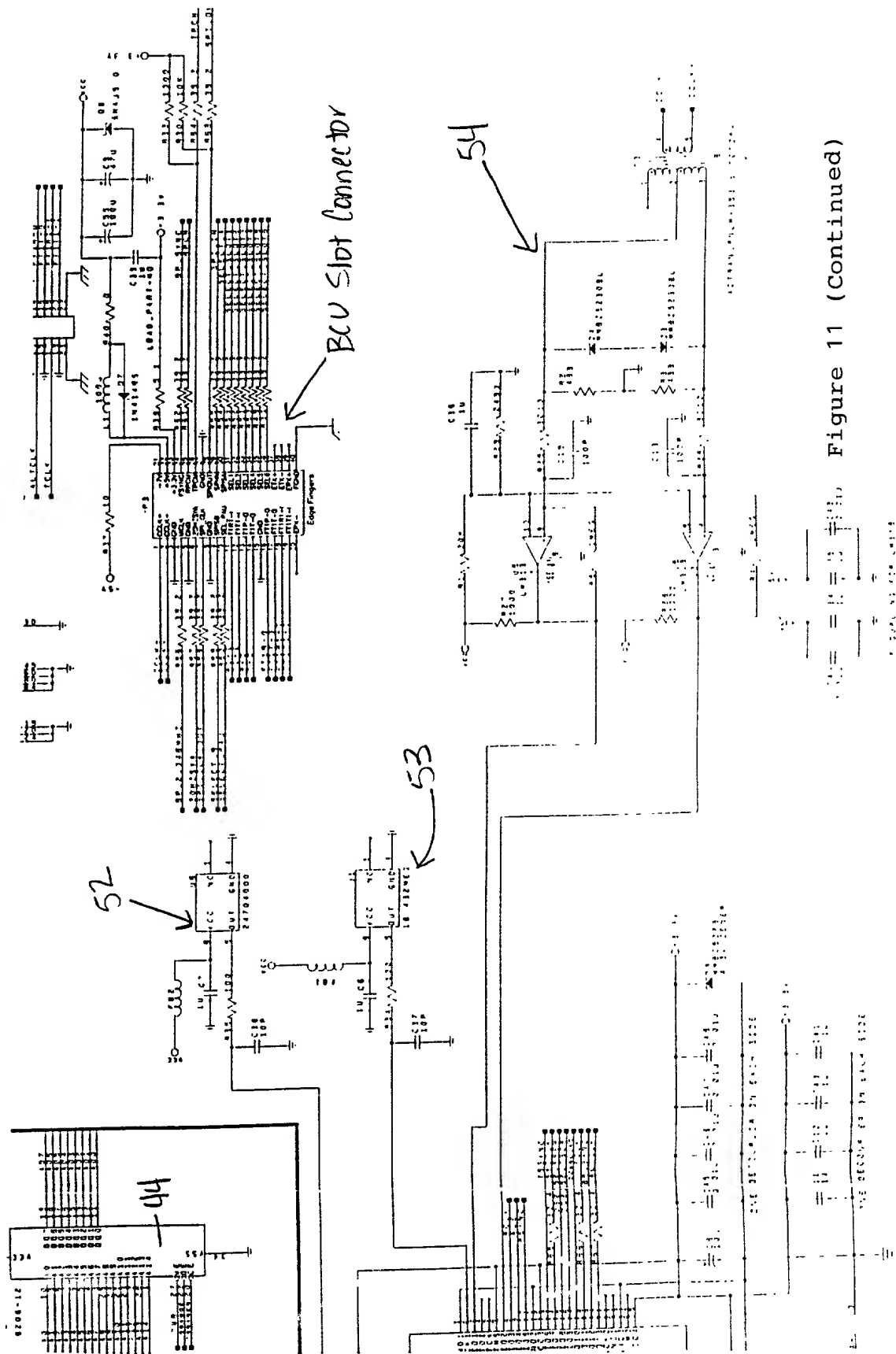


Figure 11 (Continued)

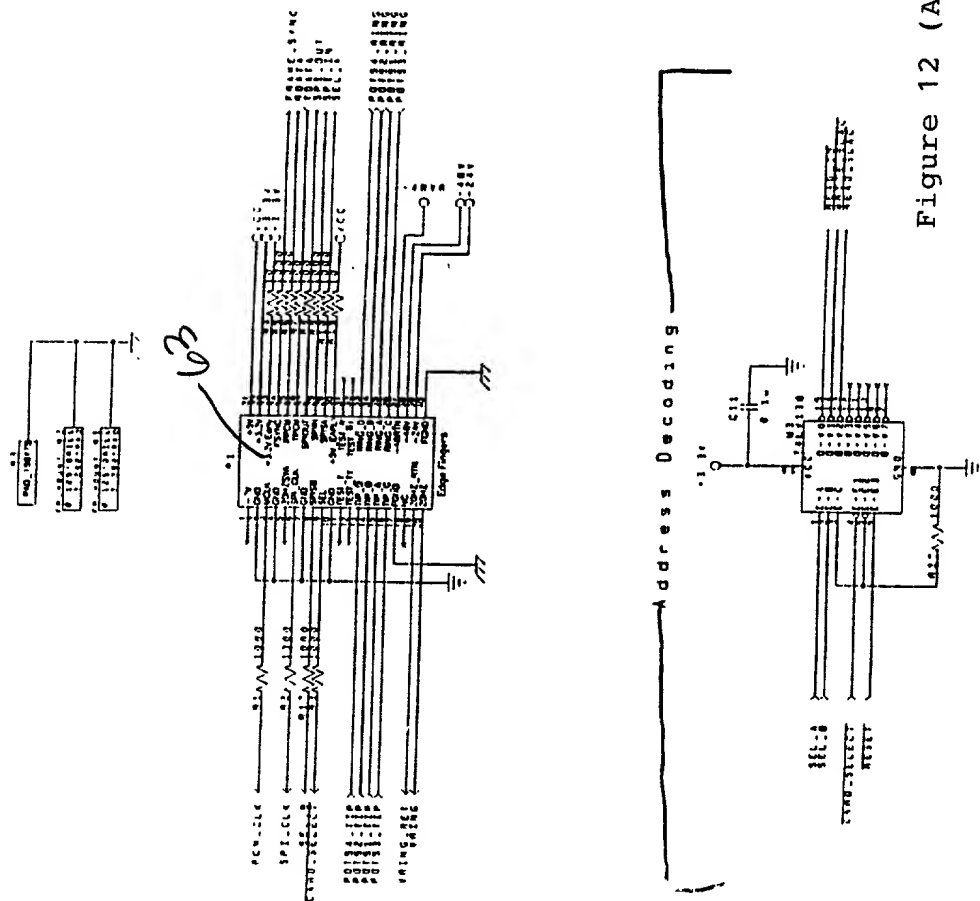
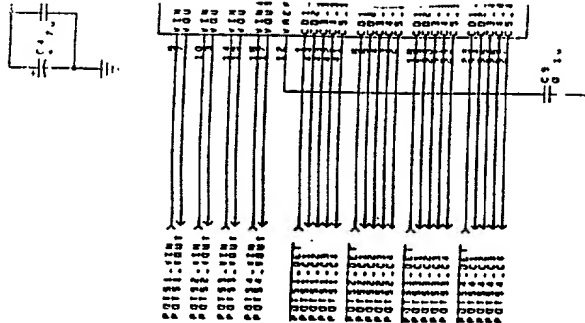
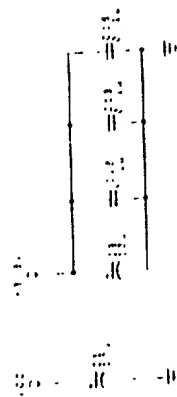
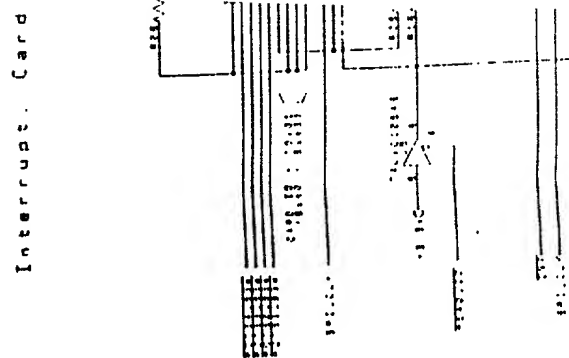
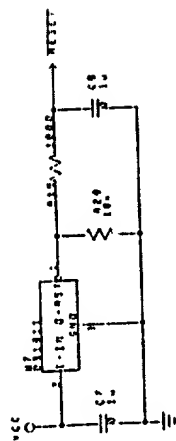
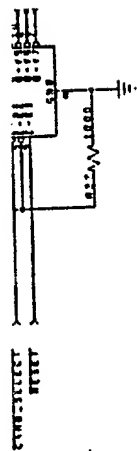


Figure 12 (A)

Q





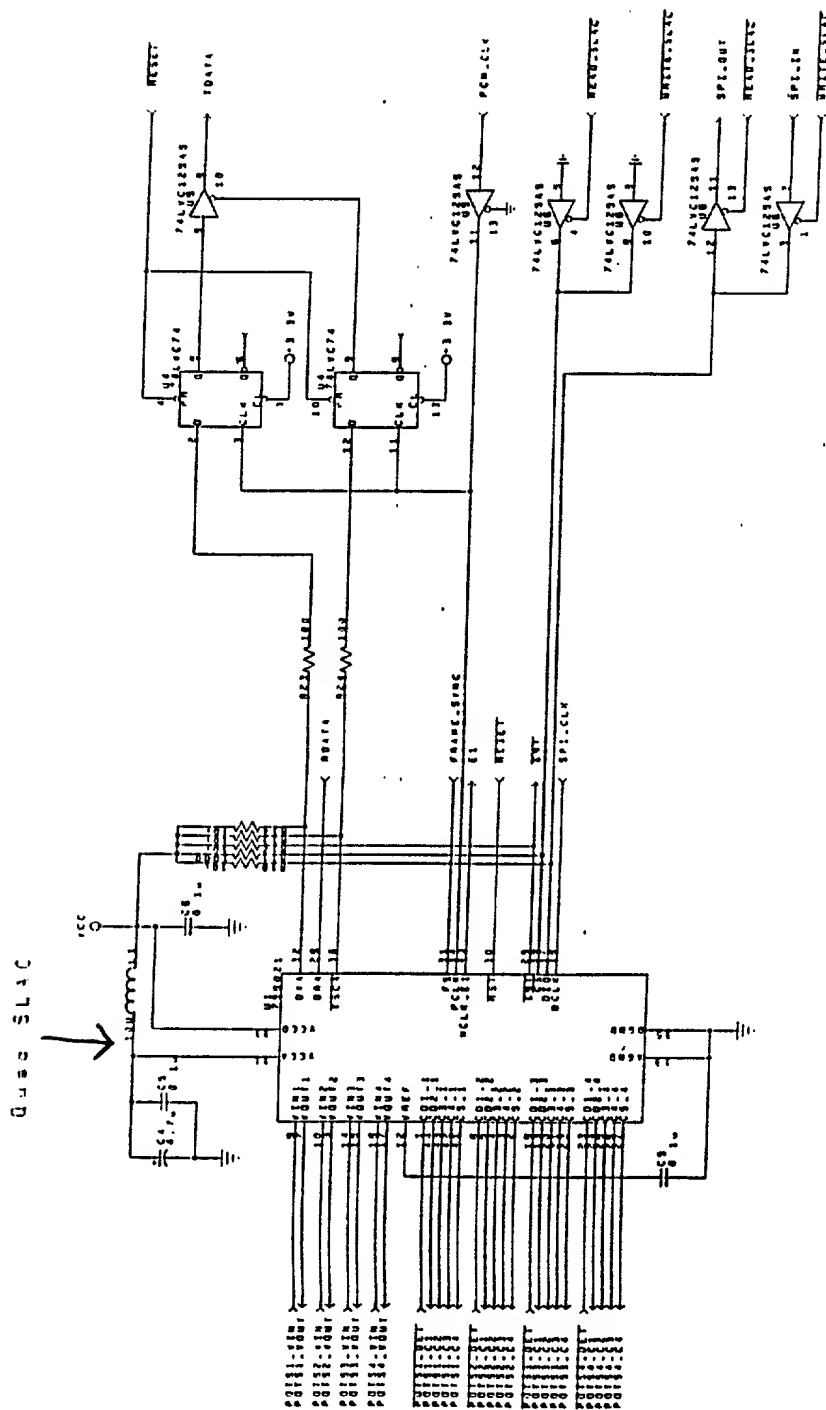


Figure 12(A) (Continued)

Interrupt, Card IO and Battery Switch Register

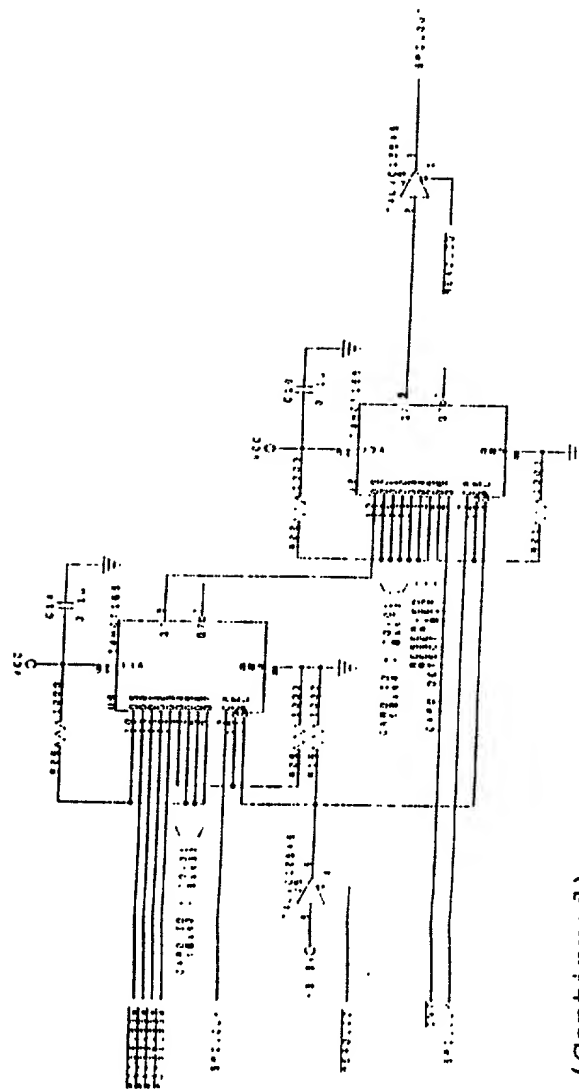


Figure 12(A) (Continued)

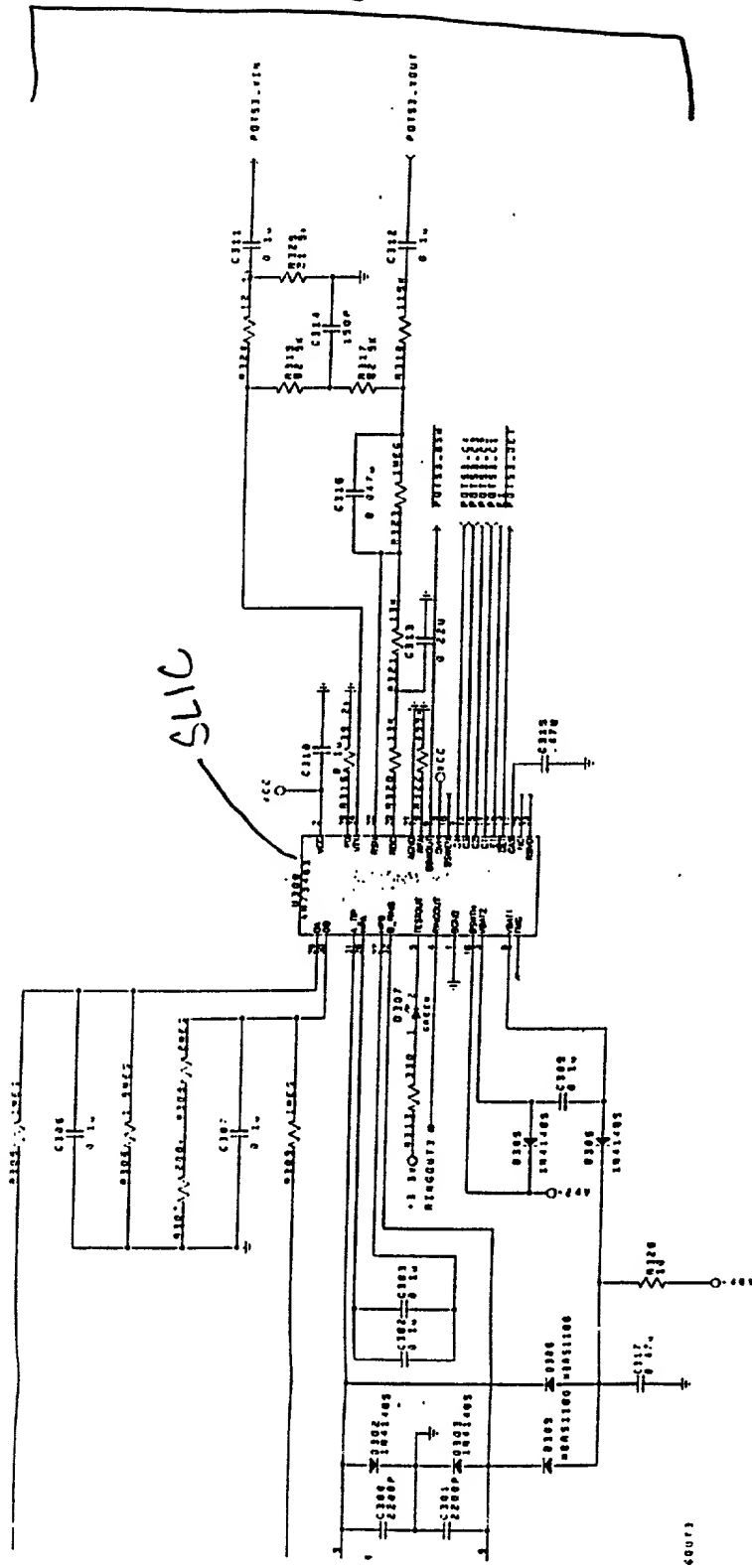
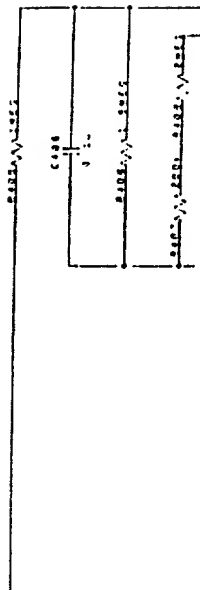


Figure 12(B)



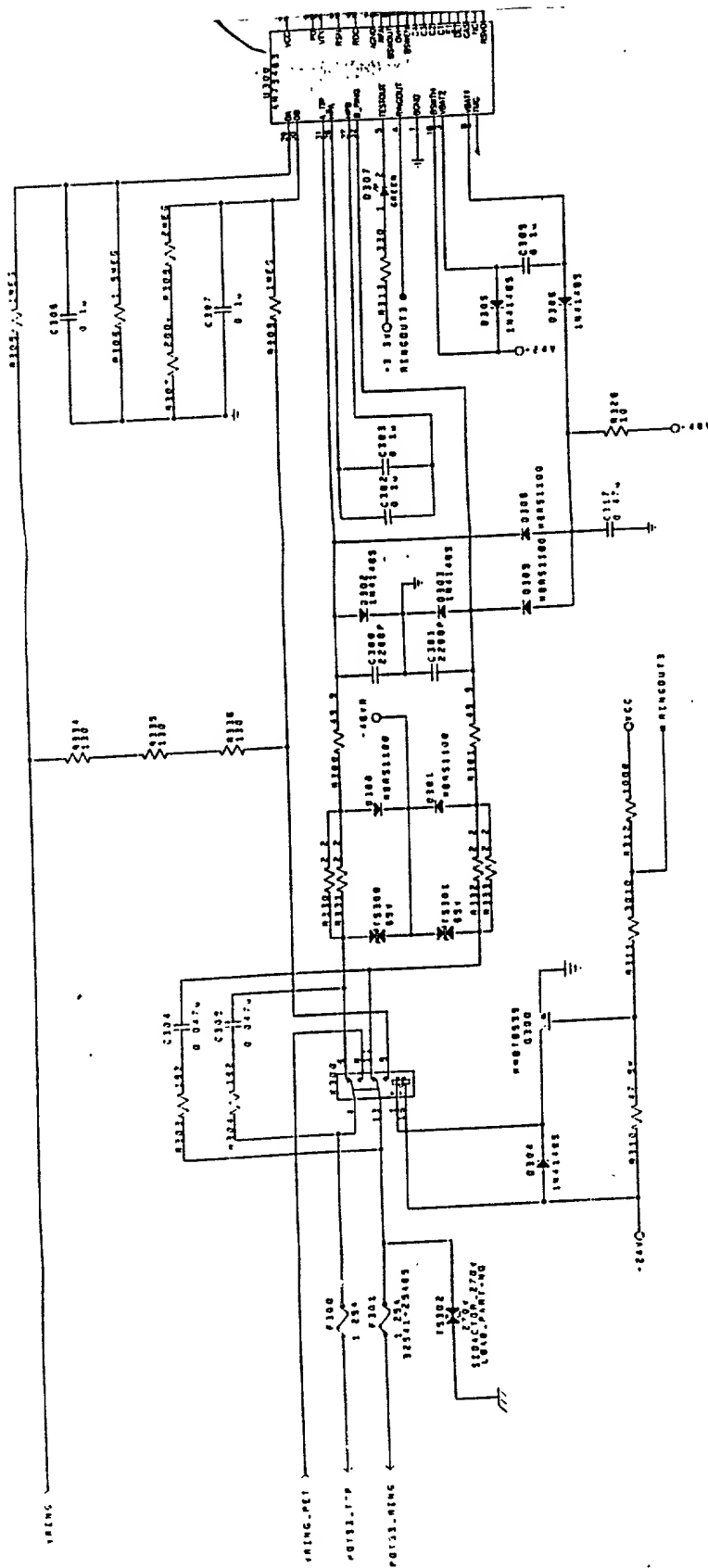


Figure 12(B) (Continued)

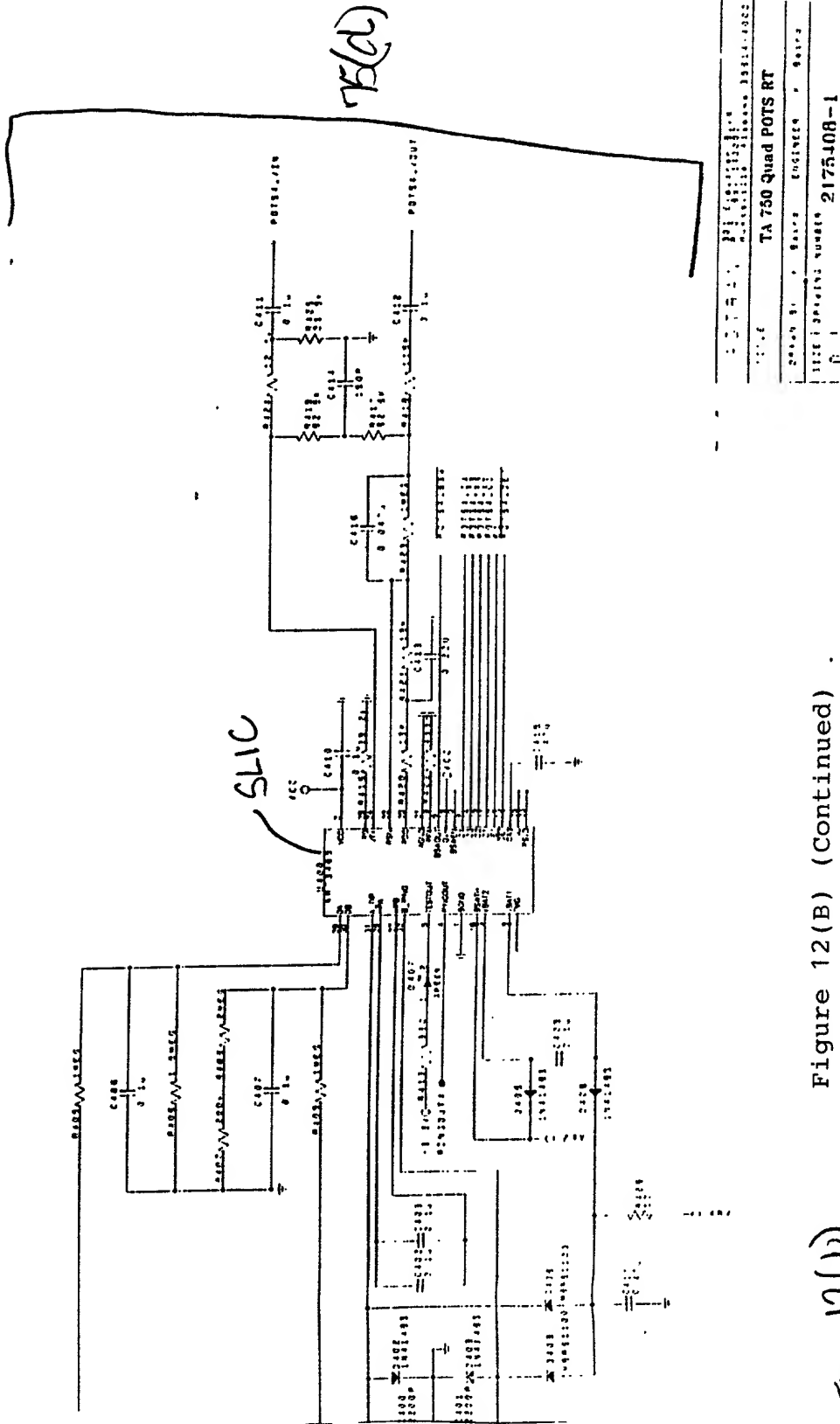


Fig. 12(b)

TA 750 Quad POTS RT
2175408-1
0

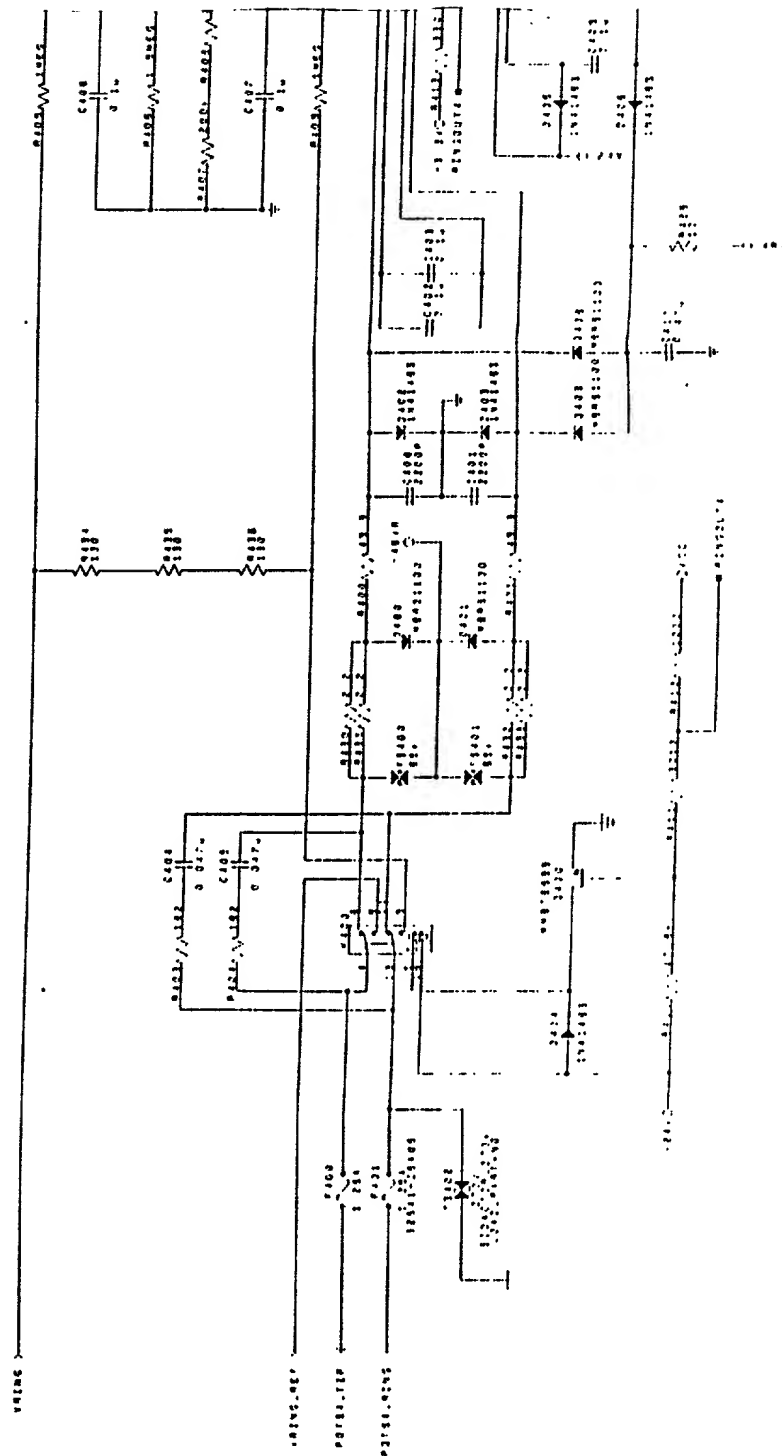


Figure 12(B) (Continued)

Fig. 12(d)

75(a)

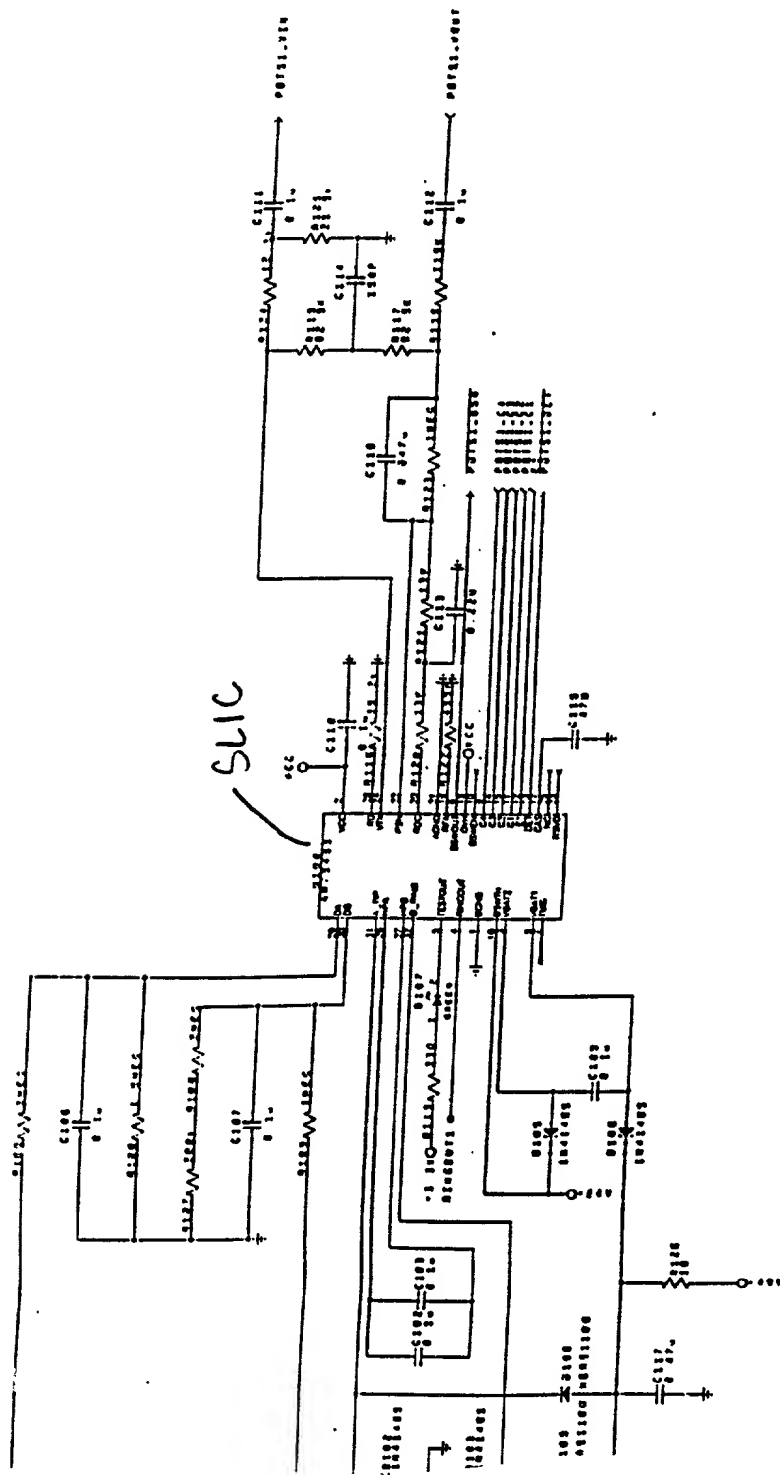


Figure 12(c)

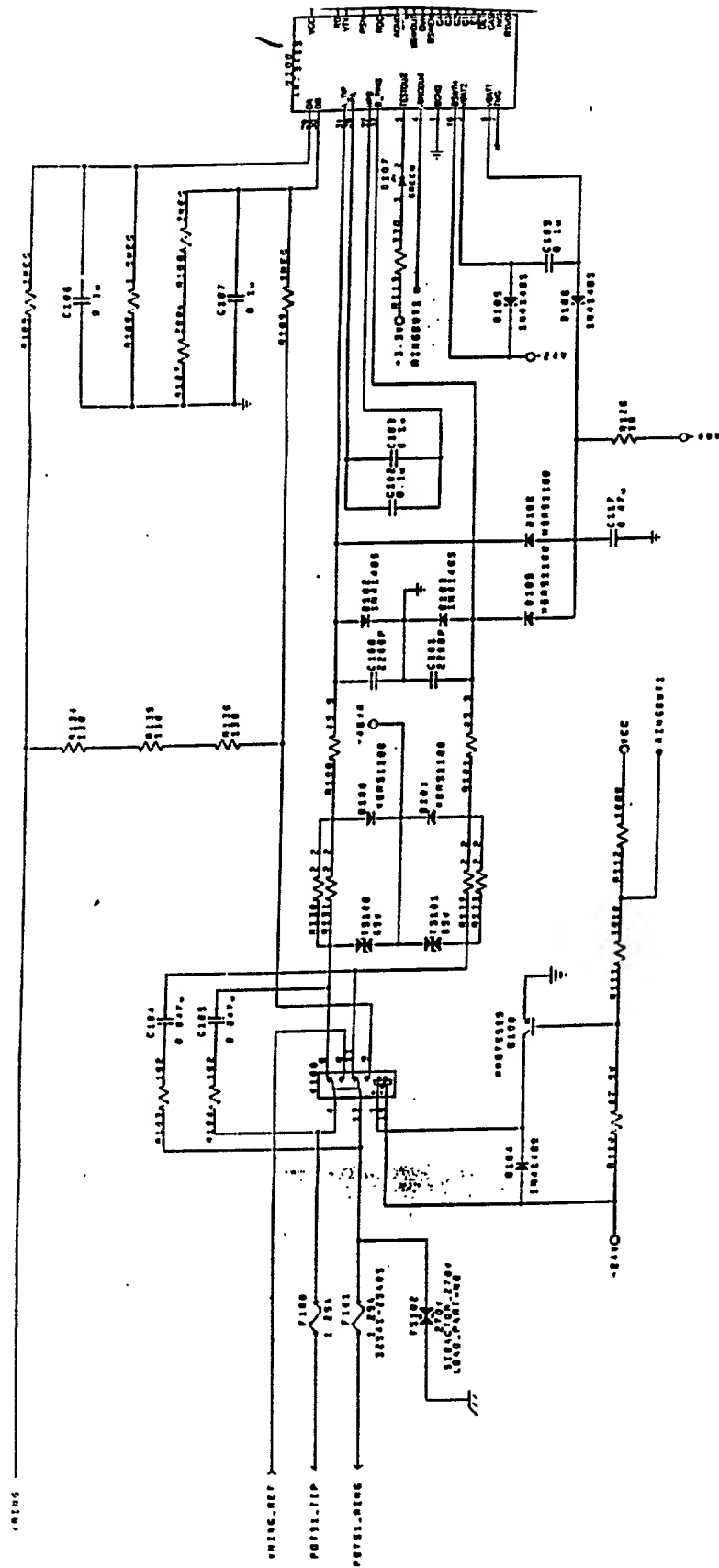


Figure 12(C) (Continued)

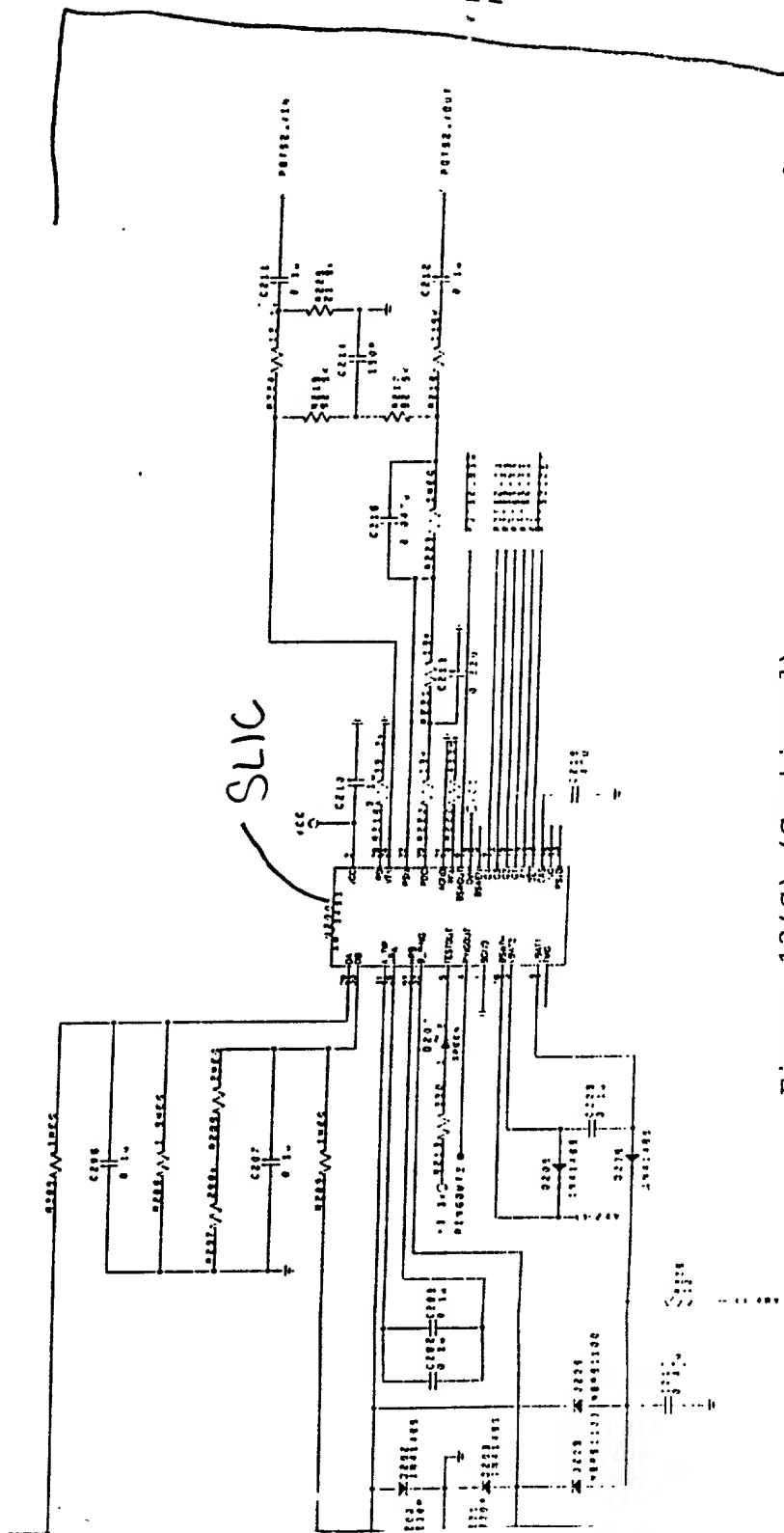
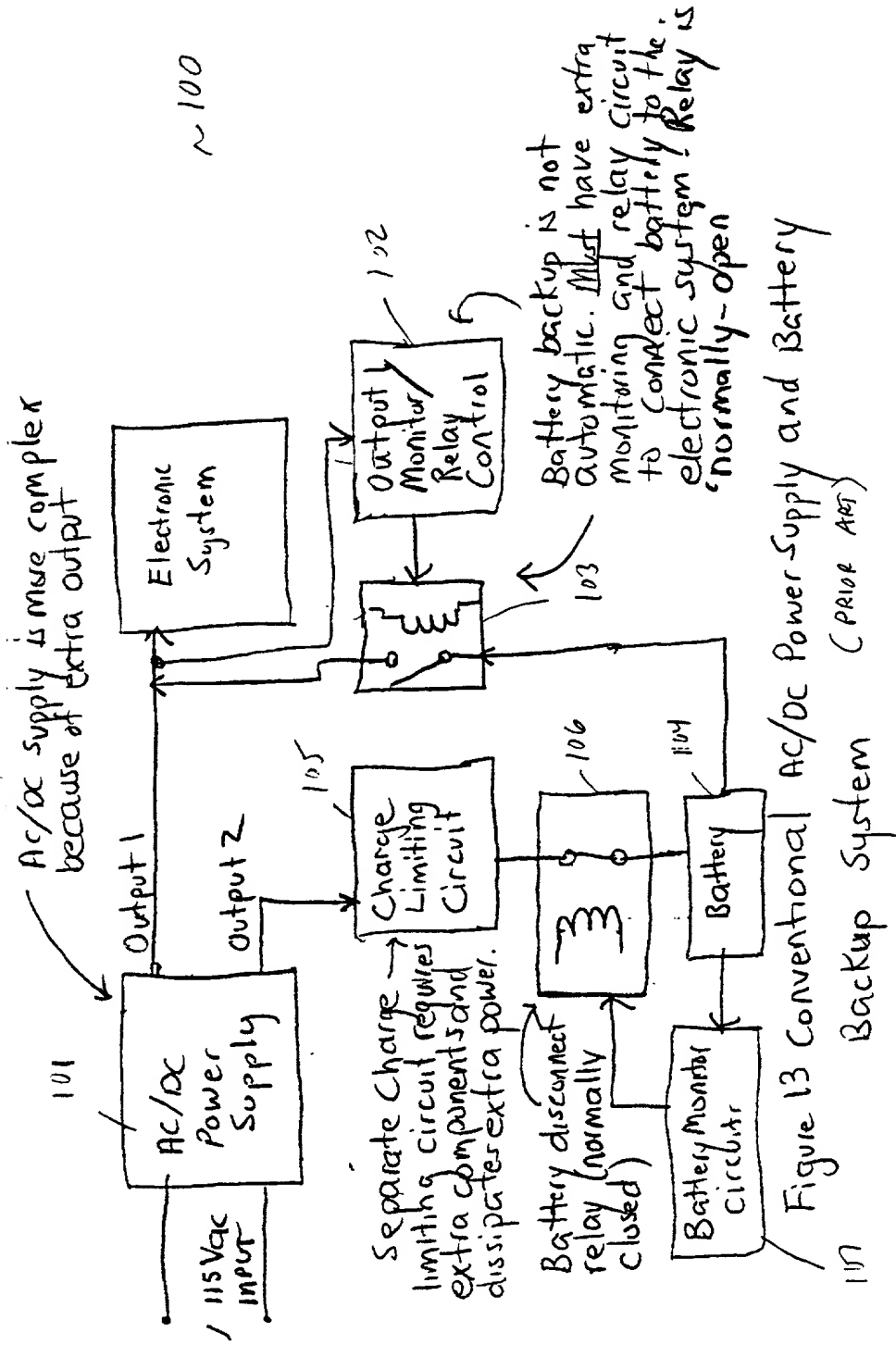
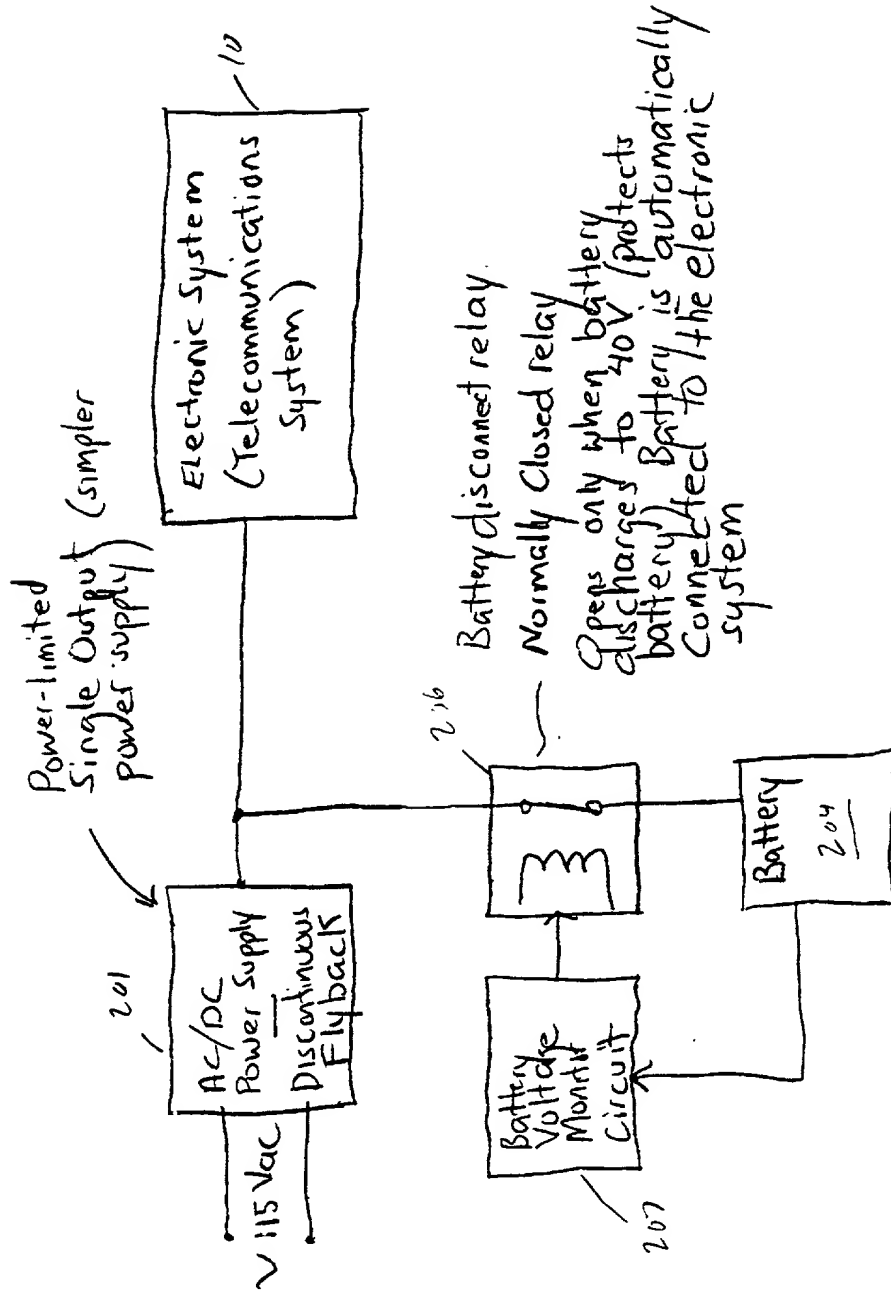


Figure 12(C) (Continued)

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~ 200

Figure 14 Block Diagram of New Power Supply/Battery Backup System

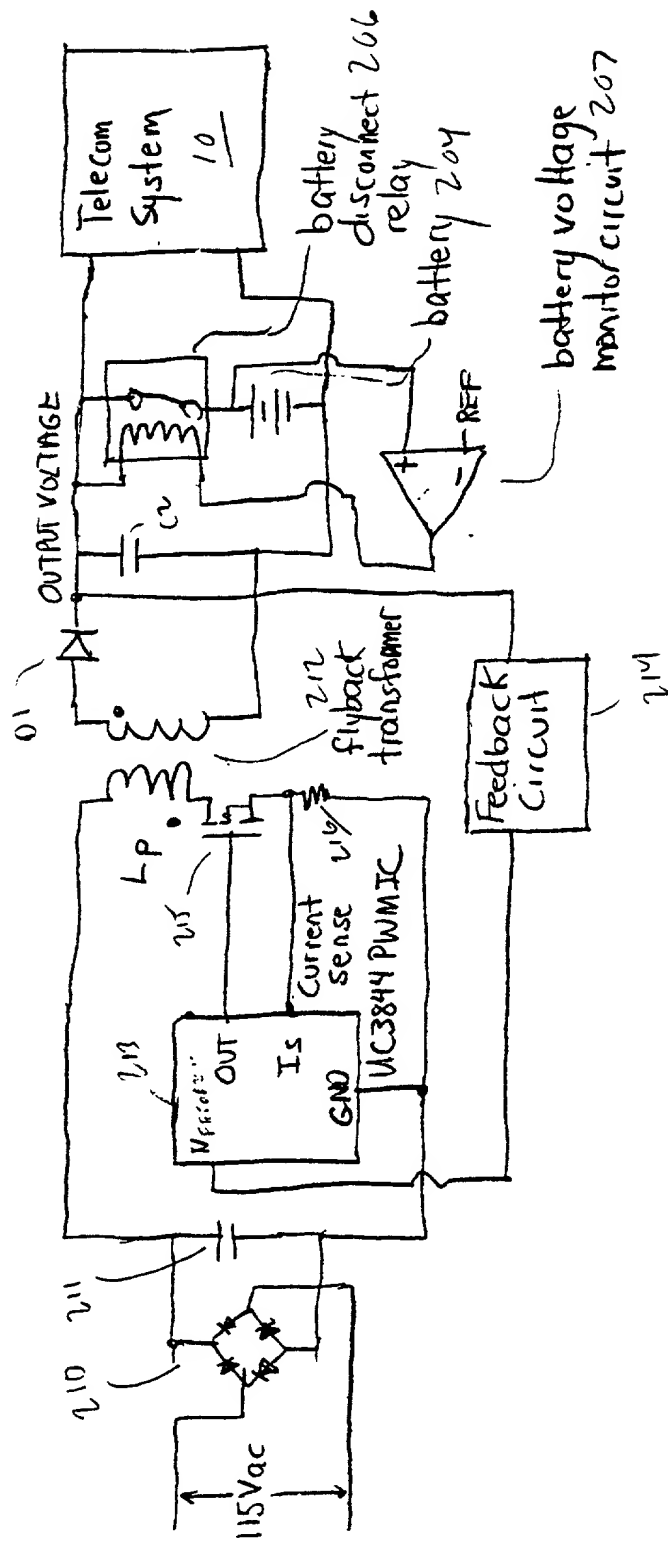


Figure 15 Simplified Schematic of New Power Supply/
Battery Backup System

Timing Diagram for SPI Receive Status Interface with all Access Modules (8 Bits)
Data coming from Access Module

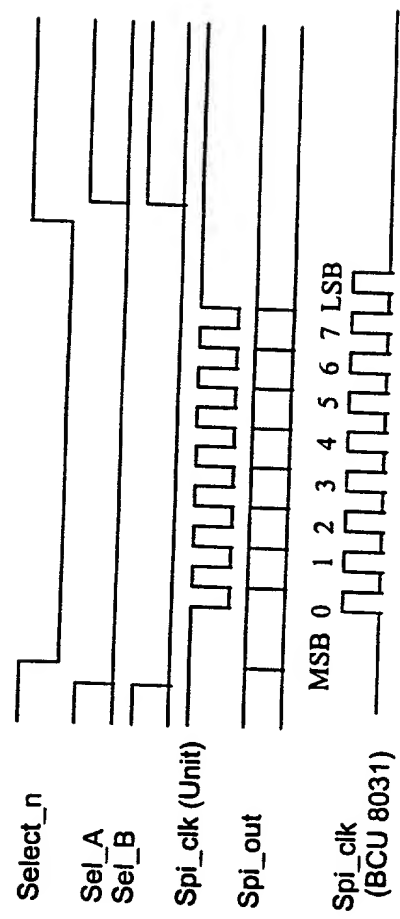


Figure 16

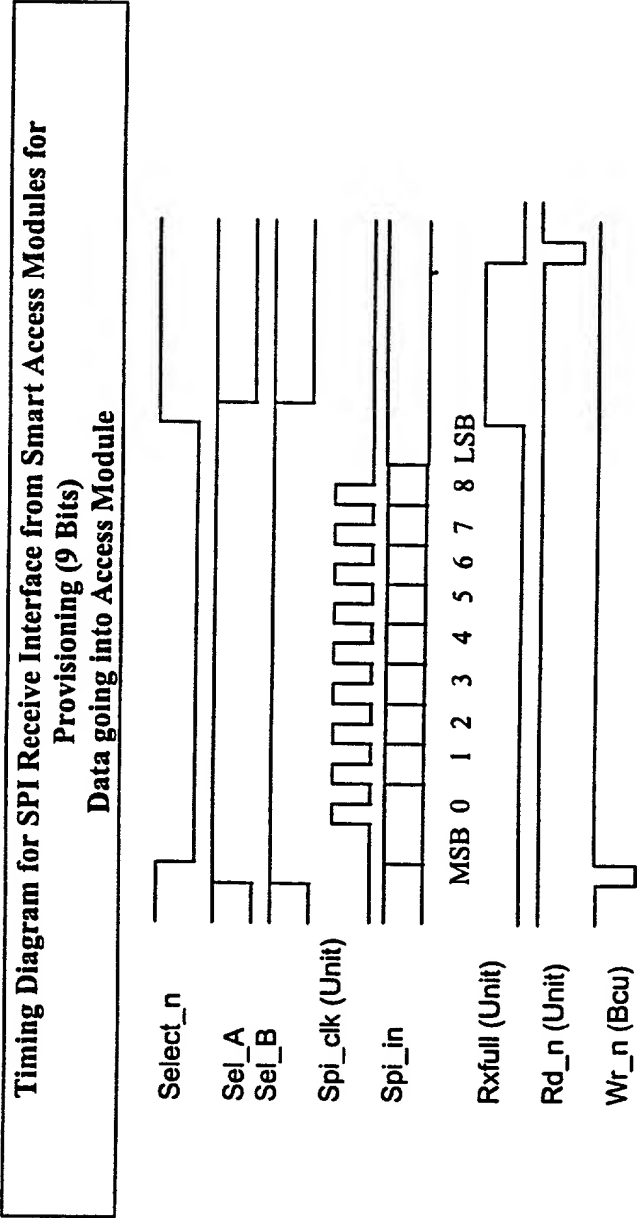


Figure 17

**Timing Diagram for SPI Transmit Interface to Smart Access Modules for Provisioning (9 Bits)
Data coming from Access Module**

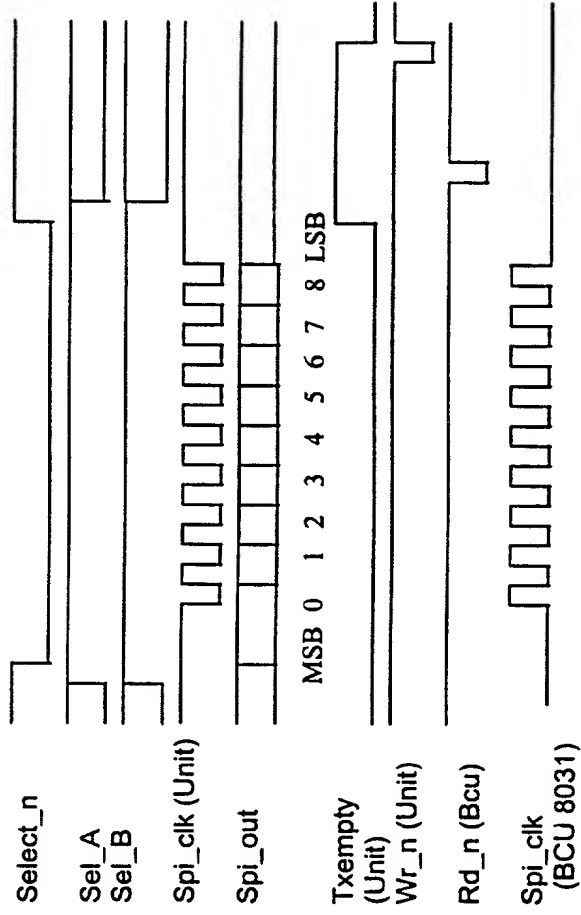


Figure 18

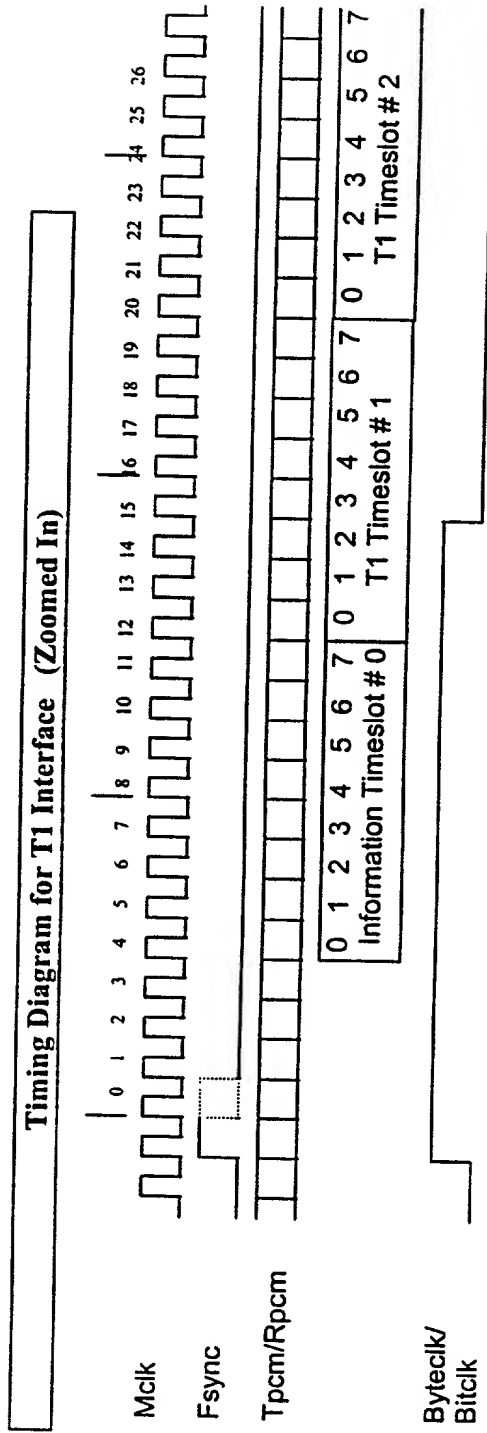


Figure 19

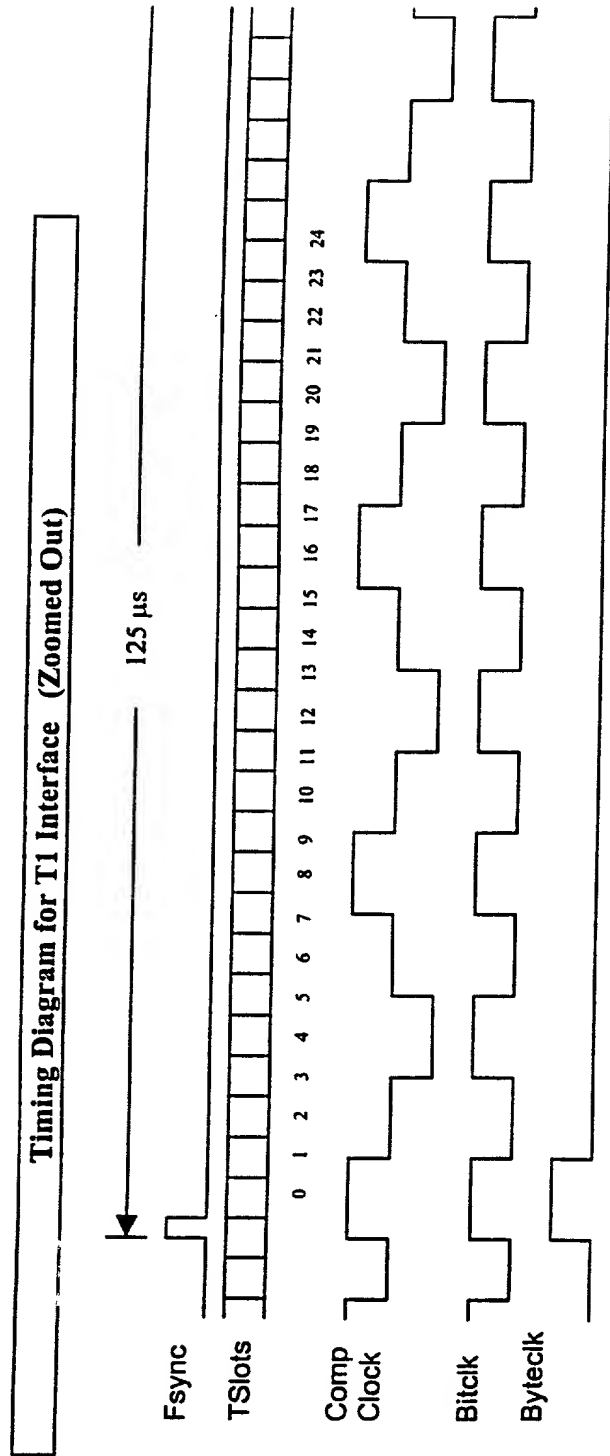


Figure 20